

Fig. 1

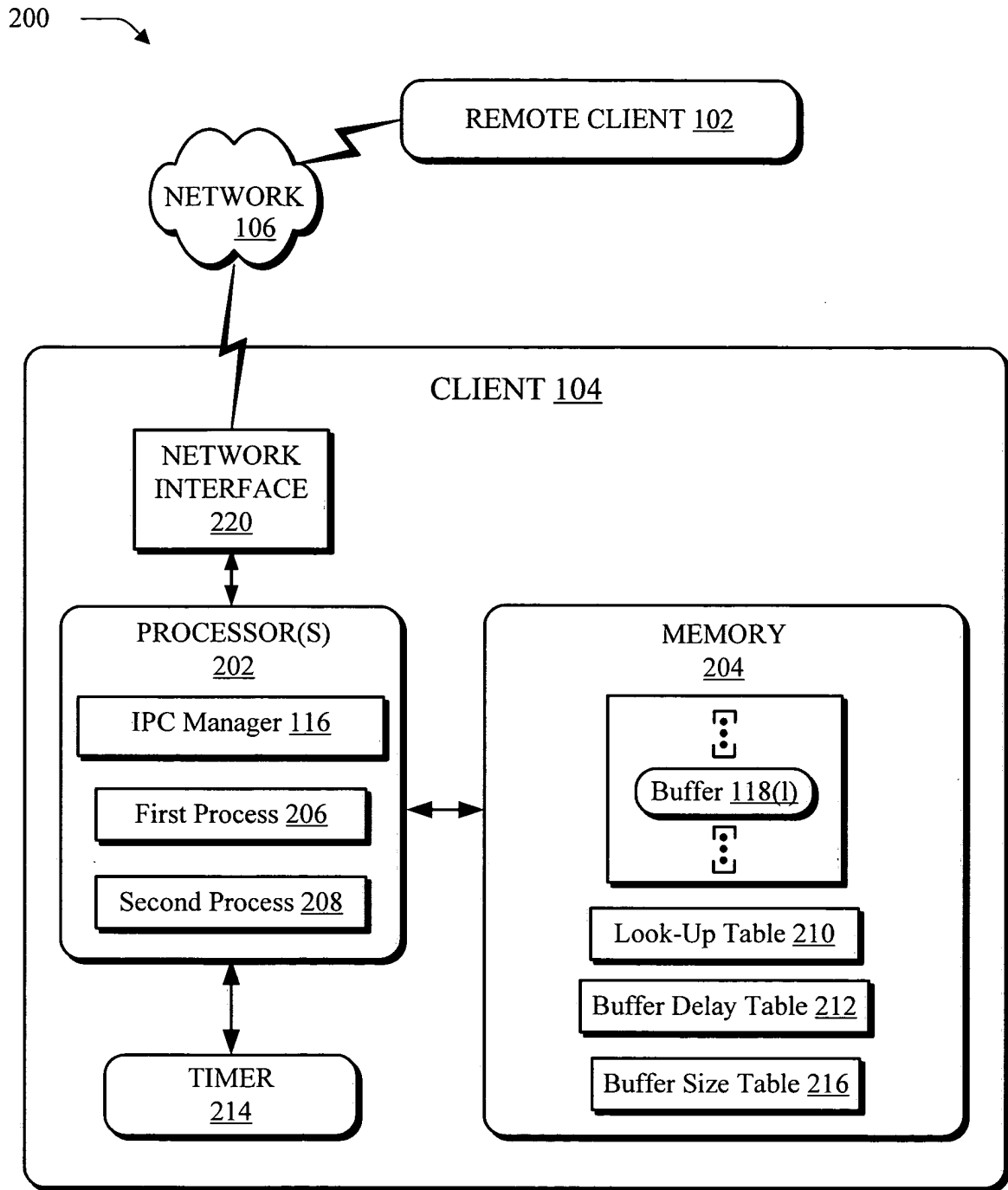


Fig. 2

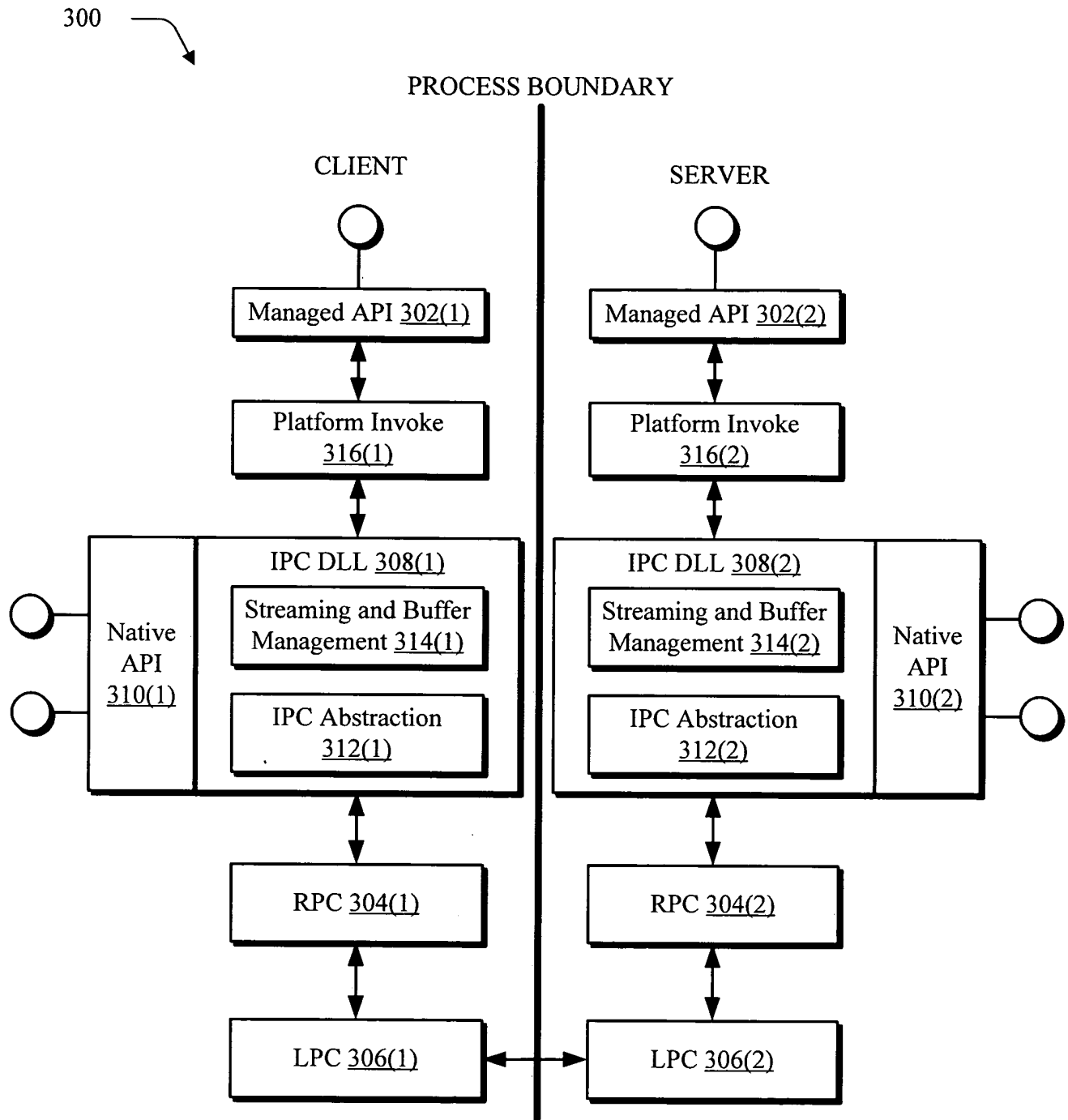


Fig. 3

400

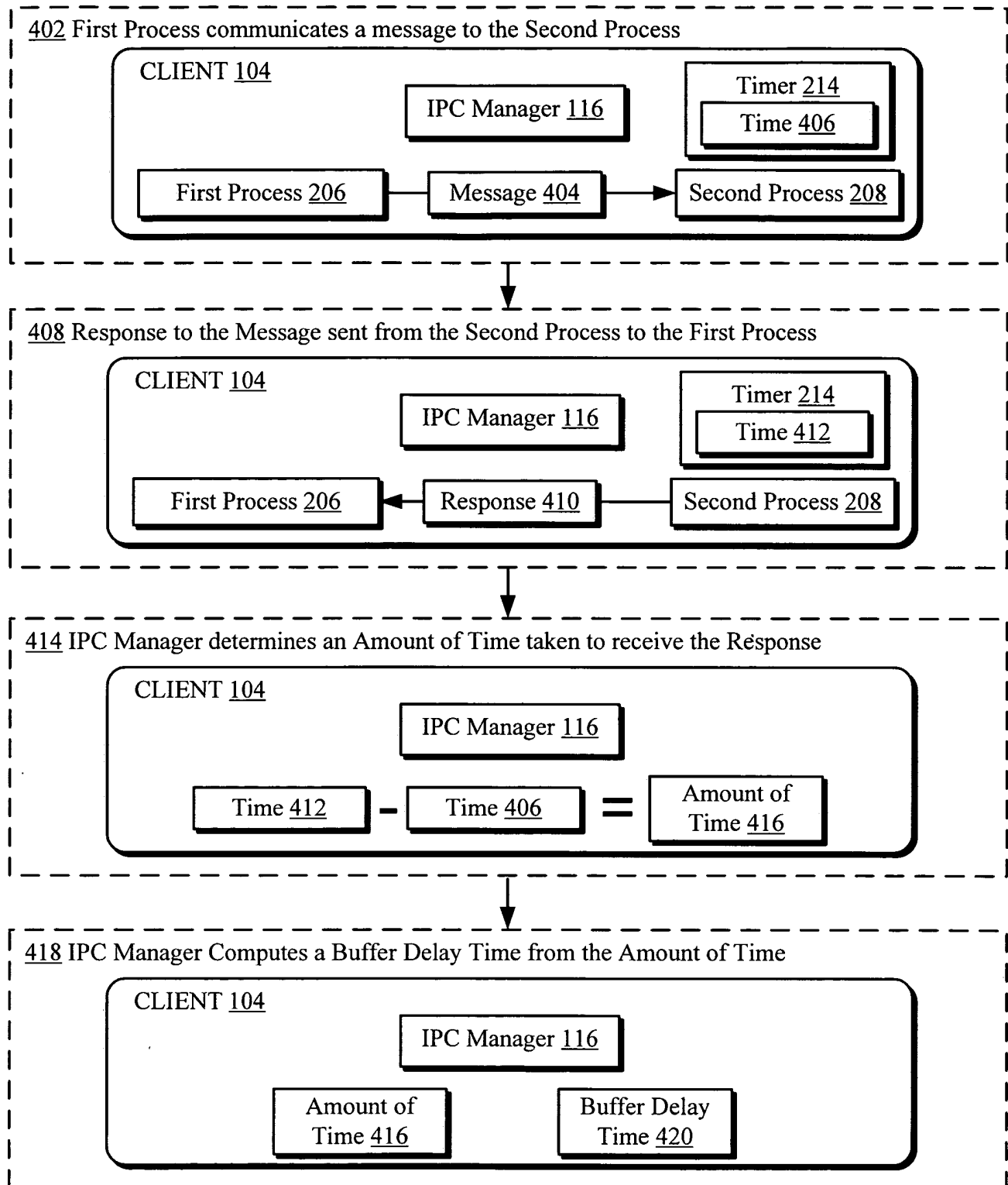


Fig. 4

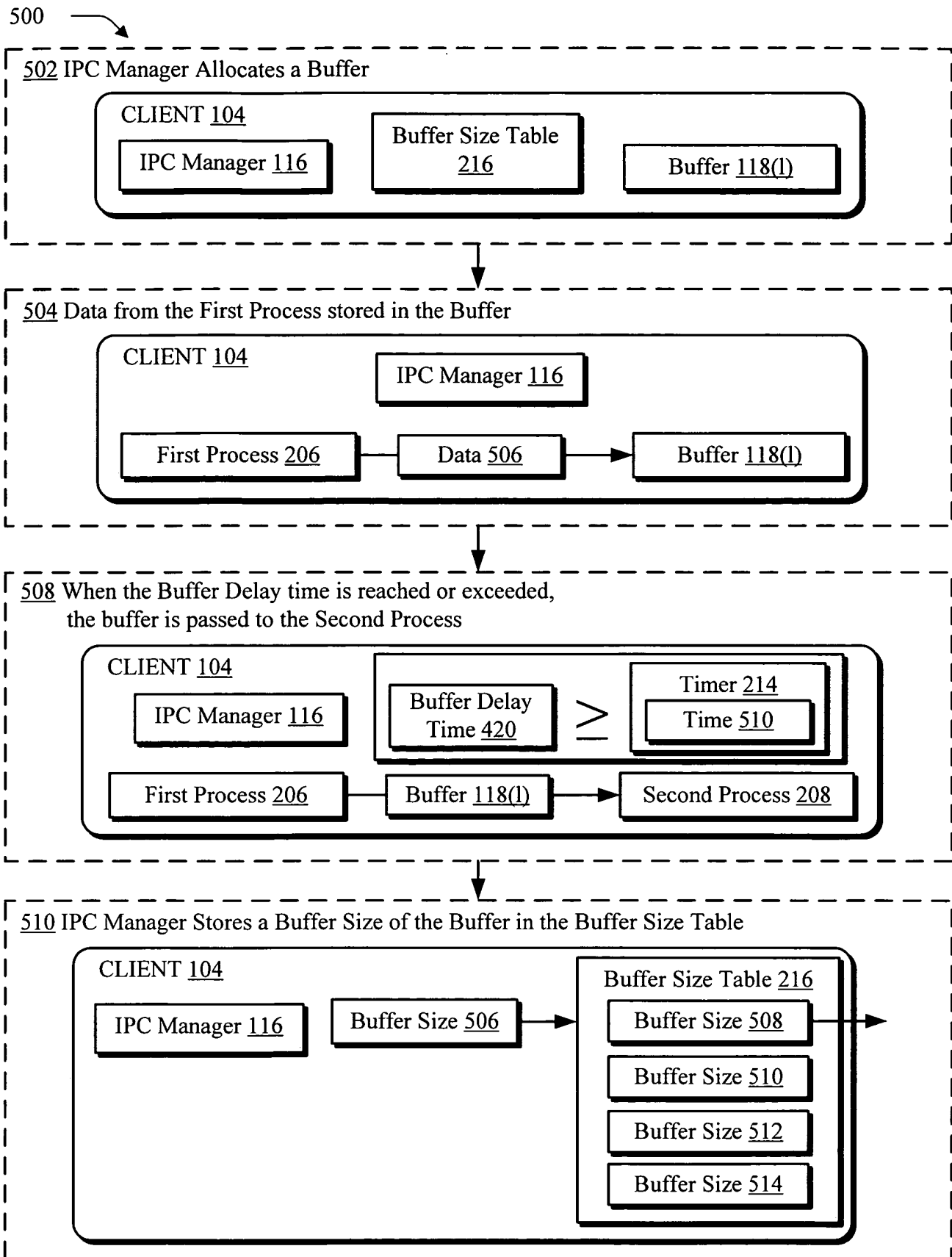


Fig. 5

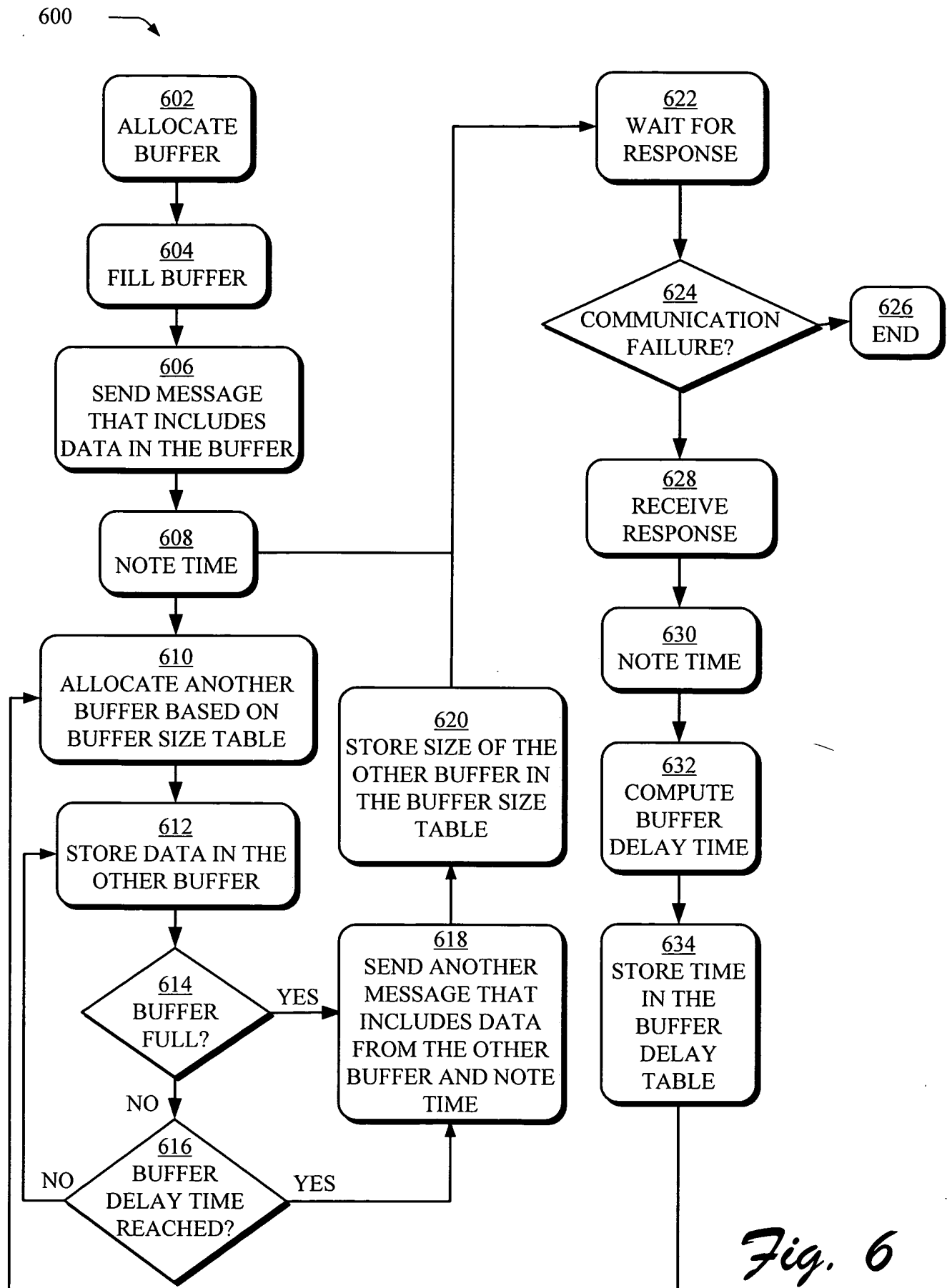


Fig. 6

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Title: Interprocess Buffer Management

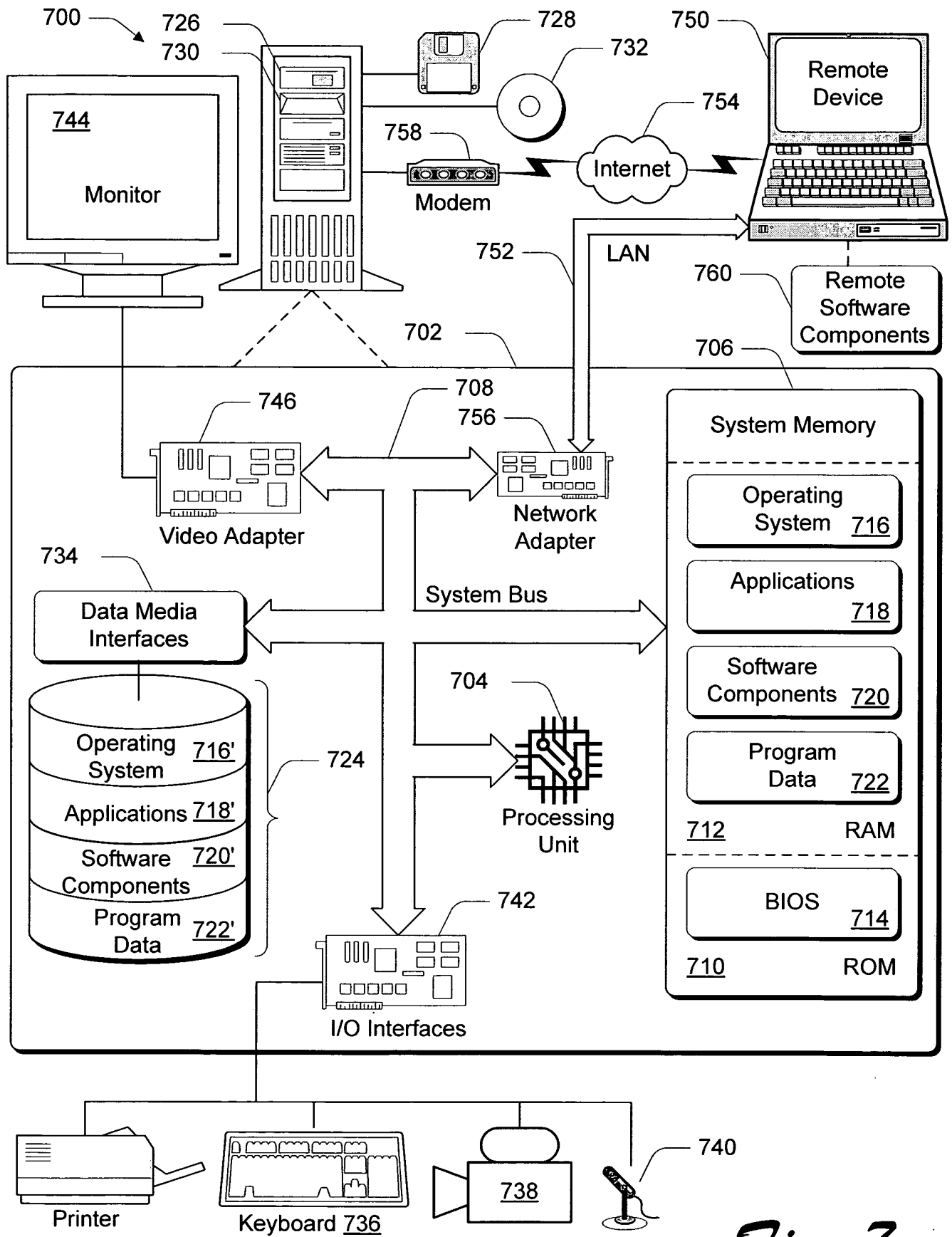


Fig. 7